

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 7,231,484 B2

APPLICATION NO. : 10/529,621

ISSUE DATE : June 12, 2007

INVENTOR(S) : Berenyi, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 4, Line 26, delete "incoming-data/information" and insert - - incoming data/information - -, therefore.

In Column 4, Line 30, delete "simultaneously" and insert - - simultaneously - -, therefore.

In Column 5, Line 44, delete "first." and insert - - first - -, therefore.

In Column 5, Line 61, delete "thereof" and insert - - thereof - -, therefore.

In Column 7, Line 23, delete "a-second" and insert - - a second - -, therefore.

In Column 10, Line 29, delete "A)" and insert - - 0) - -, therefore.

In Column 10, Line 30, delete "channel-circuits" and insert - - channel circuits - -, therefore.

In Column 18, Line 19, delete "pre-fetch/Automatic" and insert - - pre-fetch/automatic - -, therefore.

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This collection of information is required by 37 CFR 1.302, and 1.303, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patent, P.O. Box 1450, Alexandria, VA 22313-1450.

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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: U.S. Patent No. 7,231,484

USPTO CONFIRMATION CODE: 1995

APPLICATION NO.: 10/529,621

PCT FILED: September 19, 2003

US FILED: October 17, 2005

EXAMINER: Khanh Dang

GROUP ART UNIT: 2111

FOR: METHOD AND MEMORY CONTROLLER FOR SCALABLE MULTI-
CHANNEL MEMORY ACCESS

37 CFR 1.322 & 37 CFR 1.323 REQUEST FOR CERTIFICATE OF CORRECTION
FOR USPTO AND/OR APPLICANT MISTAKE

HONORABLE COMMISSIONER OF PATENTS & TRADEMARKS

SIR:

The following is a request for a certificate of correction in Serial Number 10/529,621, now Patent Number 7,231,484.

A certificate of correction under 35 USC 254 is respectfully requested in the above-identified patent.

All errors were the fault of the USPTO, no fee required. In the event that a further fee is required, please charge the amount to our Deposit Account No. 50-1379.

The exact locations where the errors appear in the patent and patent application are as follows:

In Column 4, Line 26, delete “incoming-data/information” and insert - - incoming data/information - -, therefor.
(INTERNATIONAL PUBLISHED APPLICATION, IN THE SPECIFICATION DATED MARCH 30, 2005, PAGE 6, LINE 7)

In Column 4, Line 30, delete “simultaneously” and insert - - simultaneously - -, therefor.
(INTERNATIONAL PUBLISHED APPLICATION, IN THE SPECIFICATION DATED MARCH 30, 2005, PAGE 6, LINE 10)

In Column 5, Line 44, delete “first.” and insert - - first - -, therefor.
(INTERNATIONAL PUBLISHED APPLICATION, IN THE SPECIFICATION DATED MARCH 30, 2005, PAGE 8, LINE 15)

In Column 5, Line 61, delete “thereof” and insert - - thereof - -, therefor.
(INTERNATIONAL PUBLISHED APPLICATION, IN THE SPECIFICATION DATED MARCH 30, 2005, PAGE 8, LINE 30)

In Column 7, Line 23, delete “a-second” and insert - - a second - -, therefor.
(INTERNATIONAL PUBLISHED APPLICATION, IN THE SPECIFICATION DATED MARCH 30, 2005, PAGE 11, LINE 12)

In Column 10, Line 29, delete “A)” and insert - - 0) - -, therefor.
(INTERNATIONAL PUBLISHED APPLICATION, IN THE SPECIFICATION DATED MARCH 30, 2005, PAGE 16, LINE 10)

In Column 10, Line 30, delete “channel-circuits” and insert - - channel circuits - -, therefor.
(INTERNATIONAL PUBLISHED APPLICATION, IN THE SPECIFICATION DATED MARCH 30, 2005, PAGE 16, LINES 11-12)

In Column 18, Line 19, delete “pre-fetch/Automatic” and insert - - pre-fetch/automatic - -, therefor.
(INTERNATIONAL PUBLISHED APPLICATION, IN THE SPECIFICATION DATED MARCH 30, 2005, PAGE 28, LINE 23)

The requested corrections are attached on Form PTO 1050.

Respectfully Submitted

, 2008
DATE

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Attorney of Record